Spring 2016 EE 492 Weekly Report
Week 25 4/12/2016-4/19/2016

Advisor/Client: Dr. Degang Chen
Members: Yifan Jiang (Webmaster), Scott Poder (Concept Holder), Tao Chen (Team Lead)
Project Title: Low cost high accuracy spectral test system (May1623)

Summary
In this week's meeting we talks about the problems in the ADC,and comes with backup plan
Meeting notes
Time: 4/5/2016 at 9am   **Duration:** 1 hour   **Member Present:** All

Purpose and Goals:
Try to use internal ADC and DAC from arduino due to test.

Achievements:
1. Finished the code for arduino ADC DAC testing
2. Get a new DAC

Pending issues

1. New DAC runs the same problem
2. ADC's Clock is delay

Plans for next week
Continue debug the DAC problem, and fix the ADC, prepare for the plan B

Total contributions
Yifan Jiang: 6 hours: PCB testing, programming general project research.
Scott Poder: 6 hours: PCB testing, programming general project research.
Tao Chen: 6 hours: PCB testing, programming general project research.