

Spring 2016 EE 492 Weekly Report

Week 23 3/29/2016-4/5/2016

Advisor/Client: Dr. Degang Chen

Members: Yifan Jiang (Webmaster), Scott Poder (Concept Holder), Tao Chen (Team Lead)

Project Title: Low cost high accuracy spectral test system (May1623)

Summary

In this week's meeting we talk about the problems in the DAC

Meeting notes

Time: 4/5/2016 at 9am **Duration:** 1 hour **Member Present:** All

Purpose and Goals:

Continue soldering the PCB, try to fix the DAC issue

Achievements:

1. Finished the PCB design
2. Programming on Arduino

Pending issues

1. DAC was wrong.
2. Memory Programming

Plans for next week

Fix the DAC problem

Total contributions

Yifan Jiang: 6 hours: PCB testing, programming general project research.

Scott Poder: 6 hours: PCB testing, programming general project research.

Tao Chen: 6 hours: PCB testing, programming general project research.