

Spring 2016 EE 492 Weekly Report

Week 22 3/22/2016-3/29/2016

Advisor/Client: Dr. Degang Chen

Members: Yifan Jiang (Webmaster), Scott Poder (Concept Holder), Tao Chen (Team Lead)

Project Title: Low cost high accuracy spectral test system (May1623)

Summary

In this week's meeting we come up with the testing plan, and continue soldering the PCB. arduino programming and FPGA programming

Meeting notes

Time: 3/22/2016 at 9am **Duration:** 1 hour **Member Present:** All

Purpose and Goals:

Continue soldering the PCB, and testing the parts,

Achievements:

1. Finished the PCB design
2. Finished the Sine wave lookup table

Pending issues

1. Soldering the PCB.
2. Programming

Plans for next week

Test the PCB

Total contributions

Yifan Jiang: 6 hours: PCB testing, programming general project research.

Scott Poder: 6 hours: PCB testing, programming general project research.

Tao Chen: 6 hours: PCB testing, programming general project research.