

Advisor/Client: Dr. Degang Chen

Members: Yifan Jiang (Webmaster), Scott Poder (Concept Holder), Tao Chen (Team Lead)

Project Title: Low cost high accuracy spectral test system (May1623)

Summary

We talked about component footprints, as well as how to choose resistors, capacitors, and relays.

Meeting notes

Time: 11/5/2015 at 1pm **Duration:** 1 hour **Member Present:** All

Purpose and Goals:

We presented our weekly work to advisor, and we also planned for next week's things to do.

Achievements:

1. Learned to choose resistors and capacitors we should look at their voltage/ temperature coefficient.
2. To choose a relay, its on resistance should be zero.
3. Add inverters and transceivers on the control signals' path.

Pending issues

1. Draw PCB.
2. SPI controlling between chips.

Plans for next week

1. Keep drawing PCB.
2. Find a right socket for ADC.
3. Implement Power Supply circuit for chips that do not have pre-existing power supplies in the design yet
4. Chip connection and clock for each chip

Total contributions

Yifan Jiang: 8 hours: code for input sine wave, FPGA clock divider design.

Scott Poder: 8 hours: Filter schematics design

Tao Chen: 8 hours: PCB design

This report is created by: Tao Chen on Nov 10