

Fall 2015 EE 491 Weekly Report

Week 4 9/21-9/28/2015

Advisor: Dr. Degang Chen

Members: Yifan Jiang (Webmaster), Scott Poder (Concept Holder), Tao Chen (Team Lead)

Project Title: Low cost high accuracy spectral test system (May1623)

Summary

We talked about some ADC background, as well as how to generate sine wave with DAC. We also went through Ben's Thesis and had a overview about what he did for the circuit design, and what we are going to improve based on his design.

Meeting notes

Time: 9/24/2015 at 1pm **Duration:** 1 hour **Member Present:** All

Purpose and Goals:

The agenda of this meeting was going over the background information about this design project, as well as some detail planning on our PCB.

Achievements:

1. ADC testing background knowledge were learned, such as how to read spectral plot.
2. Had idea about what we are going to put on the PCB.
3. Week planning for this week.

Pending issues

Email source person who had resources about Ben's Thesis and circuit schematic.

Plans for next week

1. Learn design PCB with OrCAD.
2. Design/ improve Ben's circuit.

Total contributions

Yifan Jiang: 5 hour

Scott Poder: 5 hour

Tao Chen: 5 hour

This report is created by: Tao Chen on Sep 28