

# Fall 2015 EE 491 Weekly Report

## Week 1 9/1-9/7/2015

---

**Advisor:** Dr. Degang Chen

**Members:** Yifan Jiang (Webmaster), Scott Poder (Concept Holder), Tao Chen (Team Lead)

**Project Title:** Low cost high accuracy spectral test system (May1623)

---

### Summary

We had our first meeting with our project advisor, we settled down the weekly meeting time and location with the advisor. We had a high level planning for this two semesters design project.

### Meeting notes

**Time:** 9/3/2015 at 1pm    **Duration:** 1 hour    **Member Present:** All

### Purpose and Goals:

Meet with advisor and make the plan of the project.

### Achievements:

The high level plan has been made. There are two phases for this project, in the first phase which is semester one, we are going to design our system and fabricate it out on the PCB before the Thanksgiving break. To make it happen, we need to learn how to design PCBs with CAD tools; be familiar with the devices that we are going to test on our PCB board, specifically, ADCs like ADS8885/ADS8881 from TI. Some things we might run into in the future, like external reference, input driver, clock signal (how to synchronize clock time on every component) and power supply. In the second phase which is semester two, we are going to debug and improve our system, as well as testing ADCs on our board.

### Pending issues

None.

### Plans for next week

1. Talking about what we've researched for our project.
2. Summarize the data sheet of ADS8885/ADS8881.
3. System design.

### Total contributions

Yifan Jiang: 1 hour

Scott Poder: 1 hour

Tao Chen: 1 hour

This report is created by: Tao Chen on Sep 7